

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	14548	speed with integrated adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/21 07:49	
2	BRS	L2	2071	1 and (flip adj flop\$1 flip-flop\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/21 07:49	
3	BRS	L3	469	2 and (flip adj flop\$1 flip-flop\$1) with delay	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/21 07:49	
4	BRS	L4	177	3 and (multiplex\$3 MUX)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/05/21 07:50	